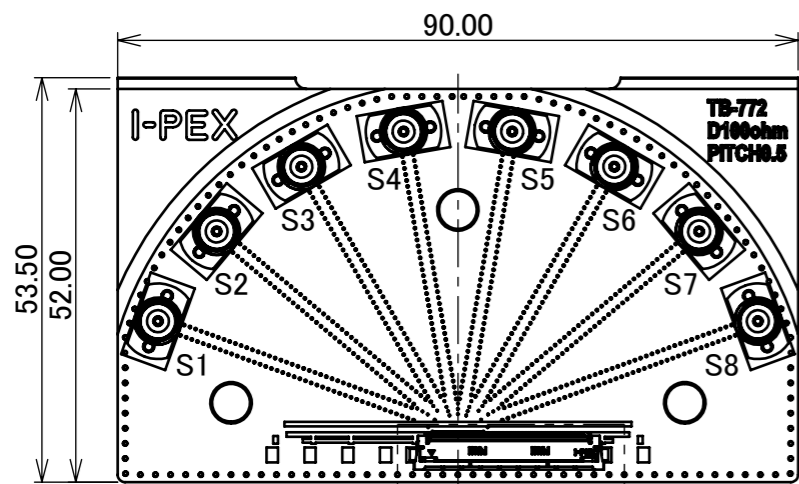
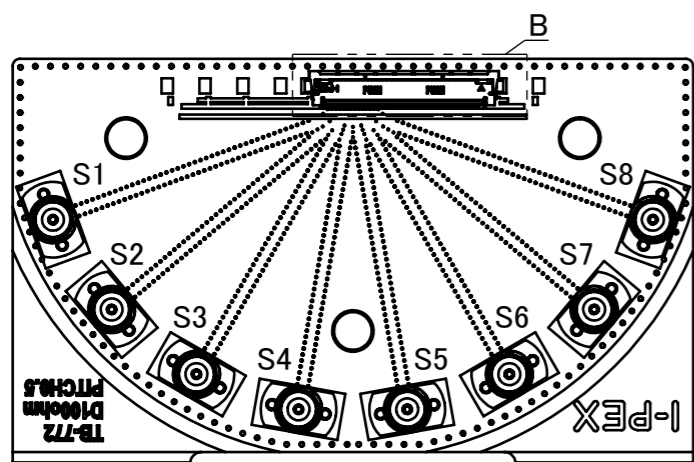


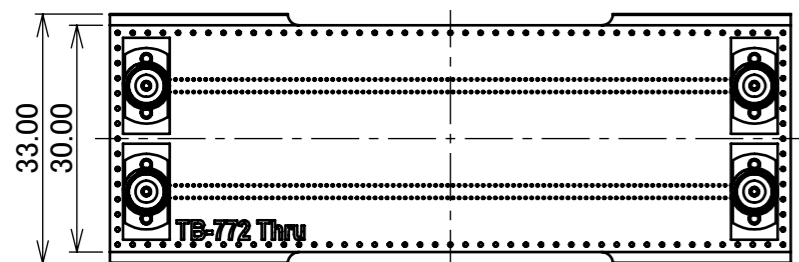
PART NO.	STRUCTURE
91570-0**B-00*	MAIN BOARD A ASSEMBLY: 1PC MAIN BOARD B ASSEMBLY: 1PC 2xTHRU BOARD: 1PC



MAIN BOARD A ASSEMBLY

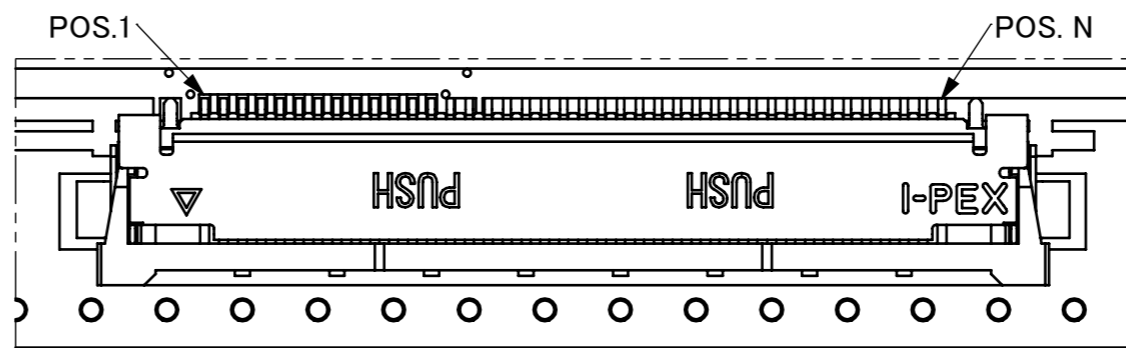


MAIN BOARD B ASSEMBLY

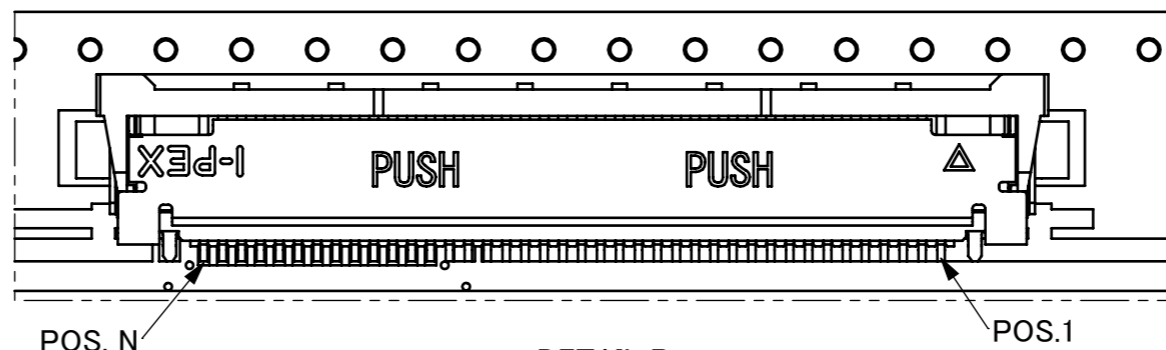


2xTHRU BOARD ASSEMBLY

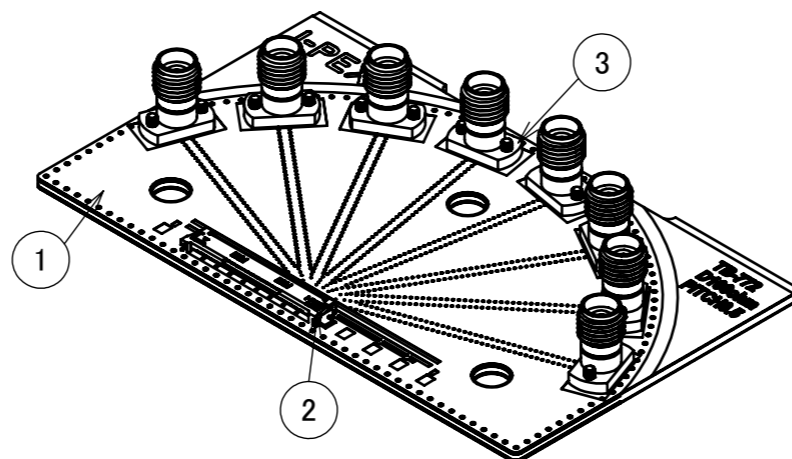
91570-0\*\*B-001  
EVAFLEX 5-VS: 20535-0\*\*E-02  
POS. 10, 15, 20, 24, 30, 40



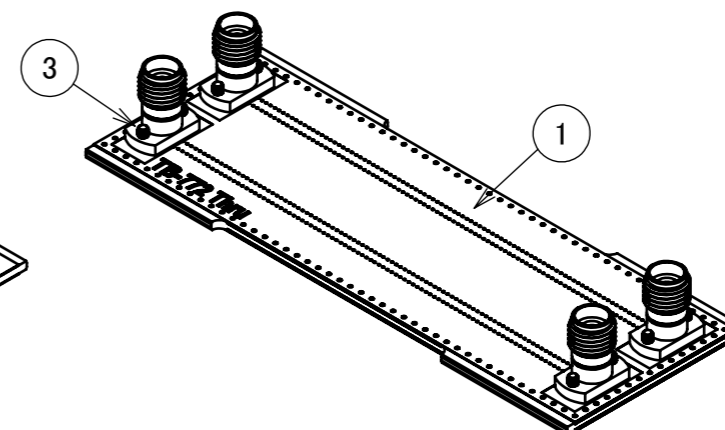
DETAIL A  
(S= 5 : 1)



DETAIL B  
(S= 5 : 1)



MAIN BOARD A/B ASSEMBLY



2xTHRU BOARD ASSEMBLY

MAIN BOARD A	
PIN POS.	ASSIGNMENT
1	GND
2	S1
3	S2
4	GND
5	S3
6	S4
7	GND
8	S5
9	S6
10	GND
11	S7
12	S8
13	GND

MAIN BOARD B	
PIN POS.	ASSIGNMENT
N	GND
N-1	S1
N-2	S2
N-3	GND
N-4	S3
N-5	S4
N-6	GND
N-7	S5
N-8	S6
N-9	GND
N-10	S7
N-11	S8
N-12	GND

3	2.92mm CONNECTOR	50104-J001	20	JACK
2	CONNECTOR	20535-0**E-02	2	EVAFLEX 5-VS RECEPTACLE
1	TEST BOARD	TB-772	1	TEST BOARD THICKNESS: 1.60mm
NO.	DESCRIPTION	PART NO.	QTY	REMARKS
				ANGLE $\pm 2^\circ$ 6 OVER 30 MAX. $\pm 0.3$ 6 MAX. $\pm 0.2$ 30 OVER 120 MAX. $\pm 0.5$
GENERAL TOLERANCE.				PROJECTION
DWG. T.Onishi DATE 2024/08/28				SERIES No. R2R6R0
CHK. M.Muro				TITLE EVAFLEX® 5-VS SI TEST BOARD ASSEMBLY
REV. ECN BY DATE APP. T.Masunaga				SCALE 1:1 UNIT mm SHEET 1/4 REV. 1
REVISION RECORD				CUSTOMER COPY

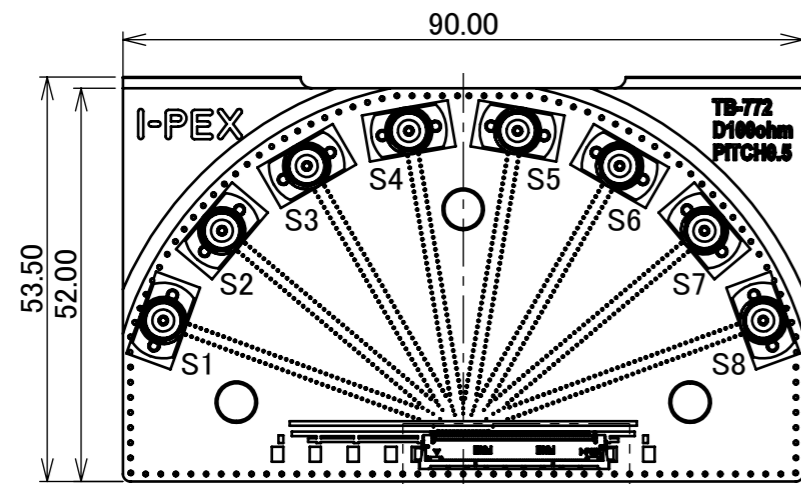
- NOTES.
- FOR DETAILED INFORMATION ON THE STRUCTURE OF TEST BOARD TB-772, REFER TO REPORT #IER-001-10705.
  - CHARACTERISTIC IMPEDANCE:  $50 \Omega \pm 10\%$  (SINGLE-ENDED),  $T_r=17.5$  ps (10% - 90%)
  - REFERENCE FFC: SEE DRAWING OF EVAFLEX 5-VS (P/N: 20535)
  - MAXIMUM MEASUREMENT FREQUENCY: 40 GHz
  - OTHER PIN CONFIGURATIONS CAN BE PROVIDED UPON REQUEST.

PART NO.	STRUCTURE
91570-0**B-00*	MAIN BOARD A ASSEMBLY: 1PC MAIN BOARD B ASSEMBLY: 1PC 2xTHRU BOARD: 1PC

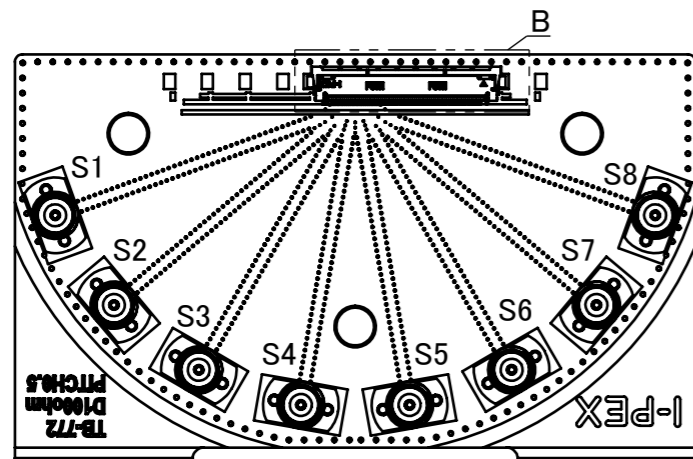
91570-0\*\*B-002  
 EVAFLEX 5-VS TYPE CH: 20720-0\*\*E-02  
 POS. 10, 15, 20, 24, 30, 40

MAIN BOARD A	
PIN POS.	ASSIGNMENT
1	GND
2	S1
3	S2
4	GND
5	S3
6	S4
7	GND
8	S5
9	S6
10	GND
11	S7
12	S8
13	GND

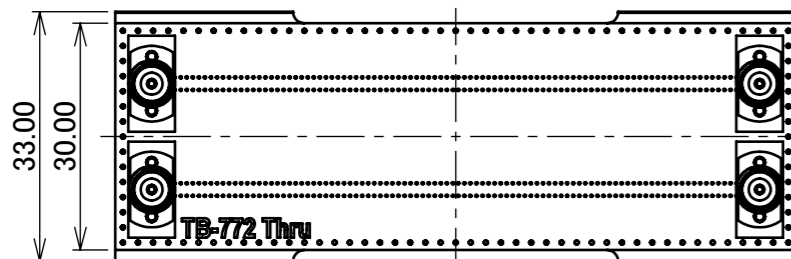
MAIN BOARD B	
PIN POS.	ASSIGNMENT
N	GND
N-1	S1
N-2	S2
N-3	GND
N-4	S3
N-5	S4
N-6	GND
N-7	S5
N-8	S6
N-9	GND
N-10	S7
N-11	S8
N-12	GND



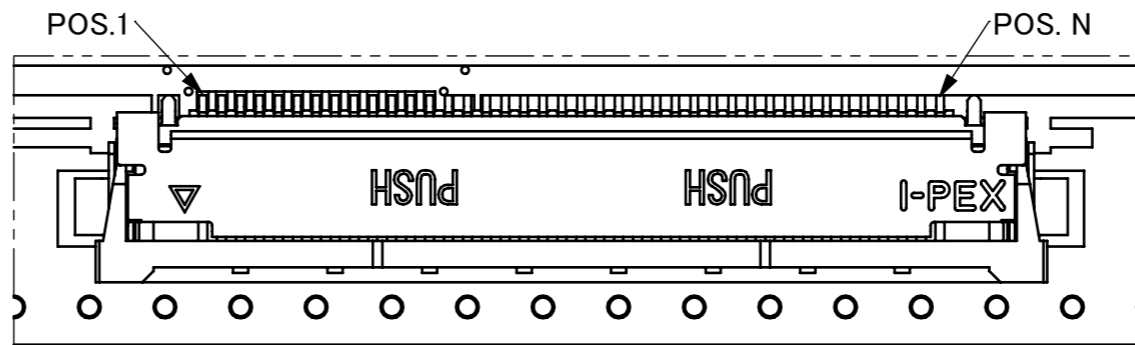
MAIN BOARD A ASSEMBLY



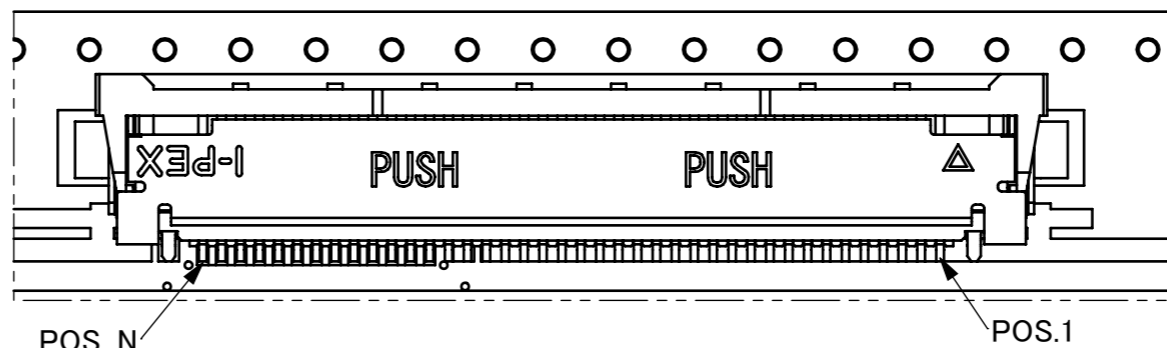
MAIN BOARD B ASSEMBLY



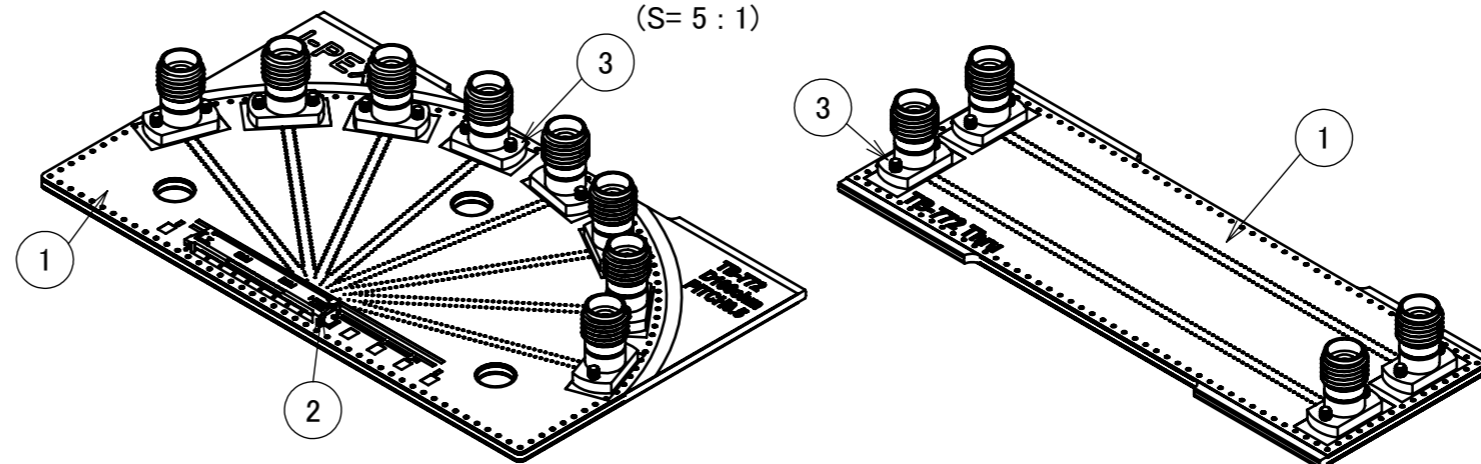
2xTHRU BOARD ASSEMBLY



DETAIL A  
(S= 5 : 1)



DETAIL B  
(S= 5 : 1)



MAIN BOARD A/B ASSEMBLY

2xTHRU BOARD ASSEMBLY

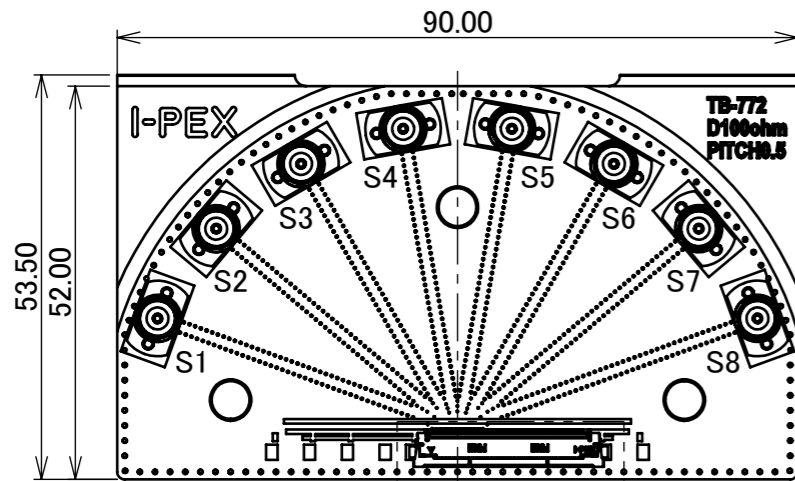
NO.	DESCRIPTION	PART NO.	QTY	REMARKS
3	2.92mm CONNECTOR	50104-J001	20	JACK
2	CONNECTOR	20720-0**E-02	2	EVAFLEX 5-VS TYPE CH RECEPTACLE
1	TEST BOARD	TB-772	1	TEST BOARD THICKNESS: 1.60mm

- NOTES.
- FOR DETAILED INFORMATION ON THE STRUCTURE OF TEST BOARD TB-772, REFER TO REPORT #IER-001-10705.
  - CHARACTERISTIC IMPEDANCE:  $50 \Omega \pm 10\%$  (SINGLE-ENDED),  $T_r=17.5 \text{ ps}$  (10% - 90%)
  - REFERENCE FFC: SEE DRAWING OF EVAFLEX 5-VS TYPE CH (P/N: 20720)
  - MAXIMUM MEASUREMENT FREQUENCY: 40 GHz
  - OTHER PIN CONFIGURATIONS CAN BE PROVIDED UPON REQUEST.

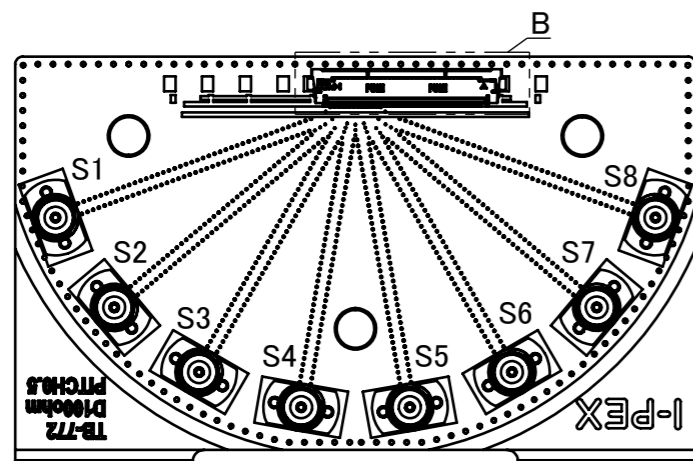
ANGLE	$\pm 2^\circ$	6 OVER 30 MAX.	$\pm 0.3$	PROJECTION 	SERIES No. R2R1R0	CUSTOMER COPY		
	6 MAX.	$\pm 0.2$	30 OVER 120 MAX.					$\pm 0.5$
GENERAL TOLERANCE.				TITLE EVAFLEX® 5-VS SI TEST BOARD ASSEMBLY	SCALE 1:1	UNIT mm	SHEET 2/4	REV. 1
DWG.	DATE							
CHK.								
APP.								
				DWG. No.	91570			

PART NO.	STRUCTURE
91570-0**B-00*	MAIN BOARD A ASSEMBLY: 1PC MAIN BOARD B ASSEMBLY: 1PC 2xTHRU BOARD: 1PC

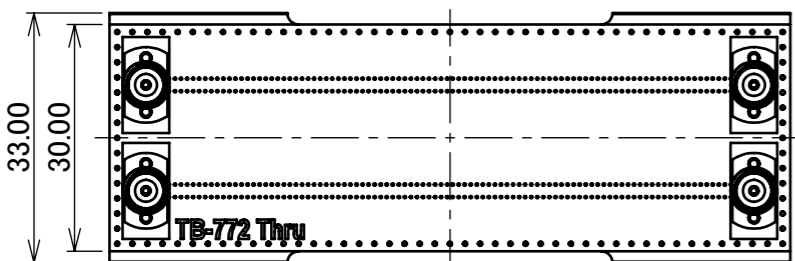
91570-0\*\*B-003  
 EVAFLEX 5-VS TYPE R: 20893-0\*\*E-02  
 POS. 30, 40, 50



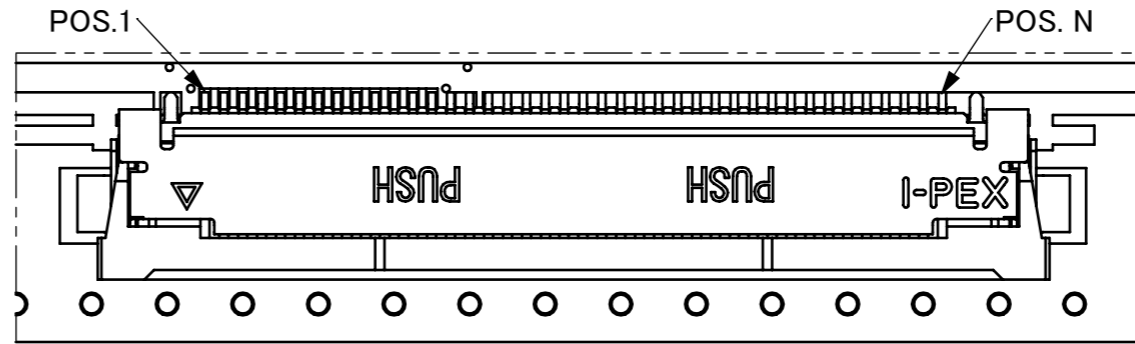
MAIN BOARD A ASSEMBLY



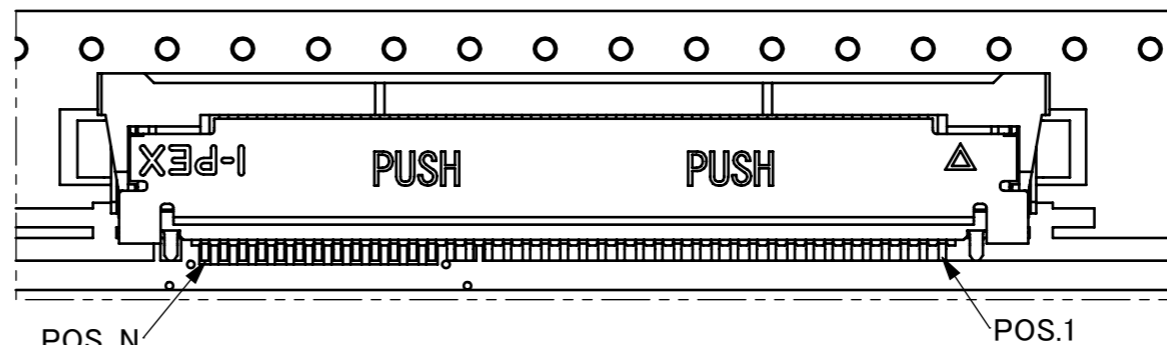
MAIN BOARD B ASSEMBLY



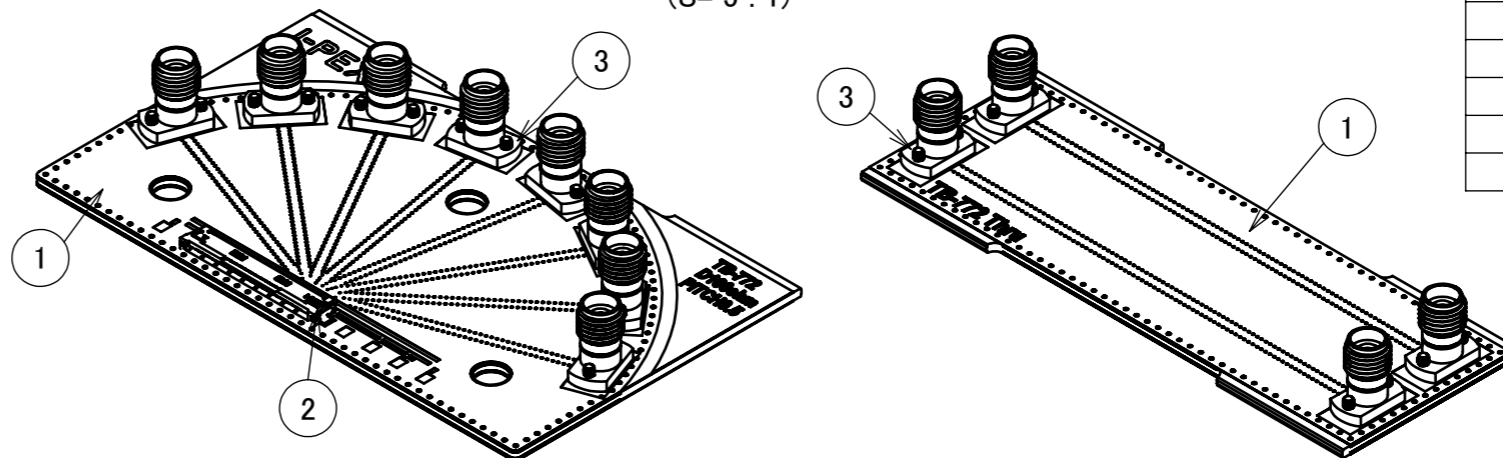
2xTHRU BOARD ASSEMBLY



DETAIL A  
(S= 5 : 1)



DETAIL B  
(S= 5 : 1)



MAIN BOARD A/B ASSEMBLY

2xTHRU BOARD ASSEMBLY

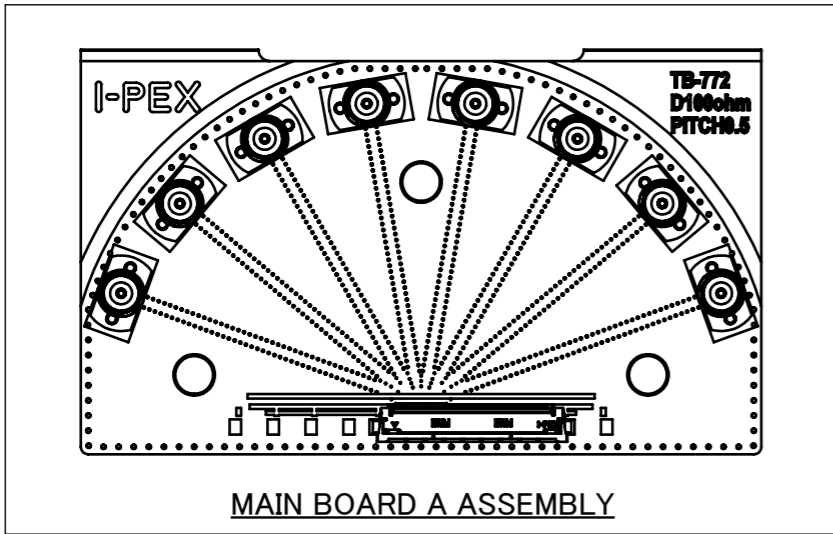
MAIN BOARD A	
PIN POS.	ASSIGNMENT
1	GND
2	S1
3	S2
4	GND
5	S3
6	S4
7	GND
8	S5
9	S6
10	GND
11	S7
12	S8
13	GND

MAIN BOARD B	
PIN POS.	ASSIGNMENT
N	GND
N-1	S1
N-2	S2
N-3	GND
N-4	S3
N-5	S4
N-6	GND
N-7	S5
N-8	S6
N-9	GND
N-10	S7
N-11	S8
N-12	GND

NO.	DESCRIPTION	PART NO.	QTY	REMARKS
3	2.92mm CONNECTOR	50104-J001	20	JACK
2	CONNECTOR	20893-0**E-02	2	EVAFLEX 5-VS TYPE R RECEPTACLE
1	TEST BOARD	TB-772	1	TEST BOARD THICKNESS: 1.60mm

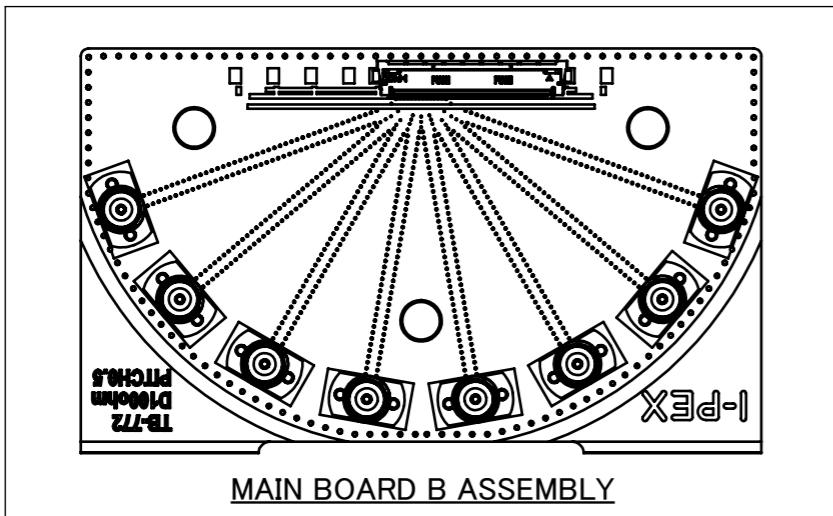
- NOTES.
- FOR DETAILED INFORMATION ON THE STRUCTURE OF TEST BOARD TB-772, REFER TO REPORT #IER-001-10705.
  - CHARACTERISTIC IMPEDANCE:  $50 \Omega \pm 10\%$  (SINGLE-ENDED),  $T_r=17.5$  ps (10% - 90%)
  - REFERENCE FFC: SEE DRAWING OF EVAFLEX 5-VS TYPE R (P/N: 20893)
  - MAXIMUM MEASUREMENT FREQUENCY: 40 GHz
  - OTHER PIN CONFIGURATIONS CAN BE PROVIDED UPON REQUEST.

ANGLE	$\pm 2^\circ$	6 OVER 30 MAX.	$\pm 0.3$	PROJECTION 	SERIES No. R2R0R0	CUSTOMER COPY		
	6 MAX.	$\pm 0.2$	30 OVER 120 MAX.					$\pm 0.5$
GENERAL TOLERANCE.				TITLE EVAFLEX® 5-VS SI TEST BOARD ASSEMBLY	SCALE 1:1	UNIT mm	SHEET 3/4	REV. 1
DWG.	DATE							
CHK.								
APP.								
				DWG. No.	91570			

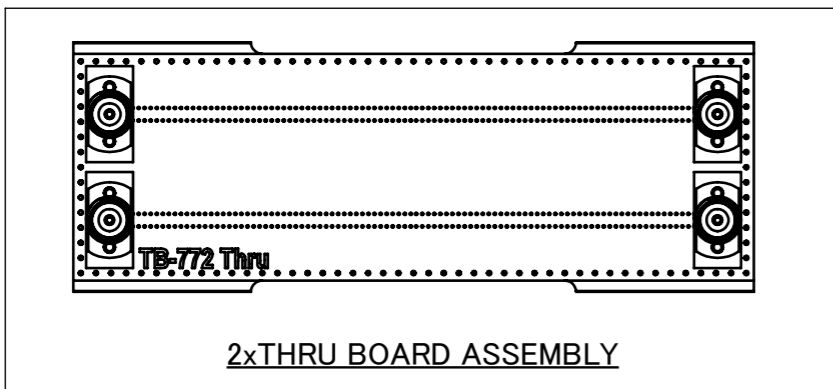


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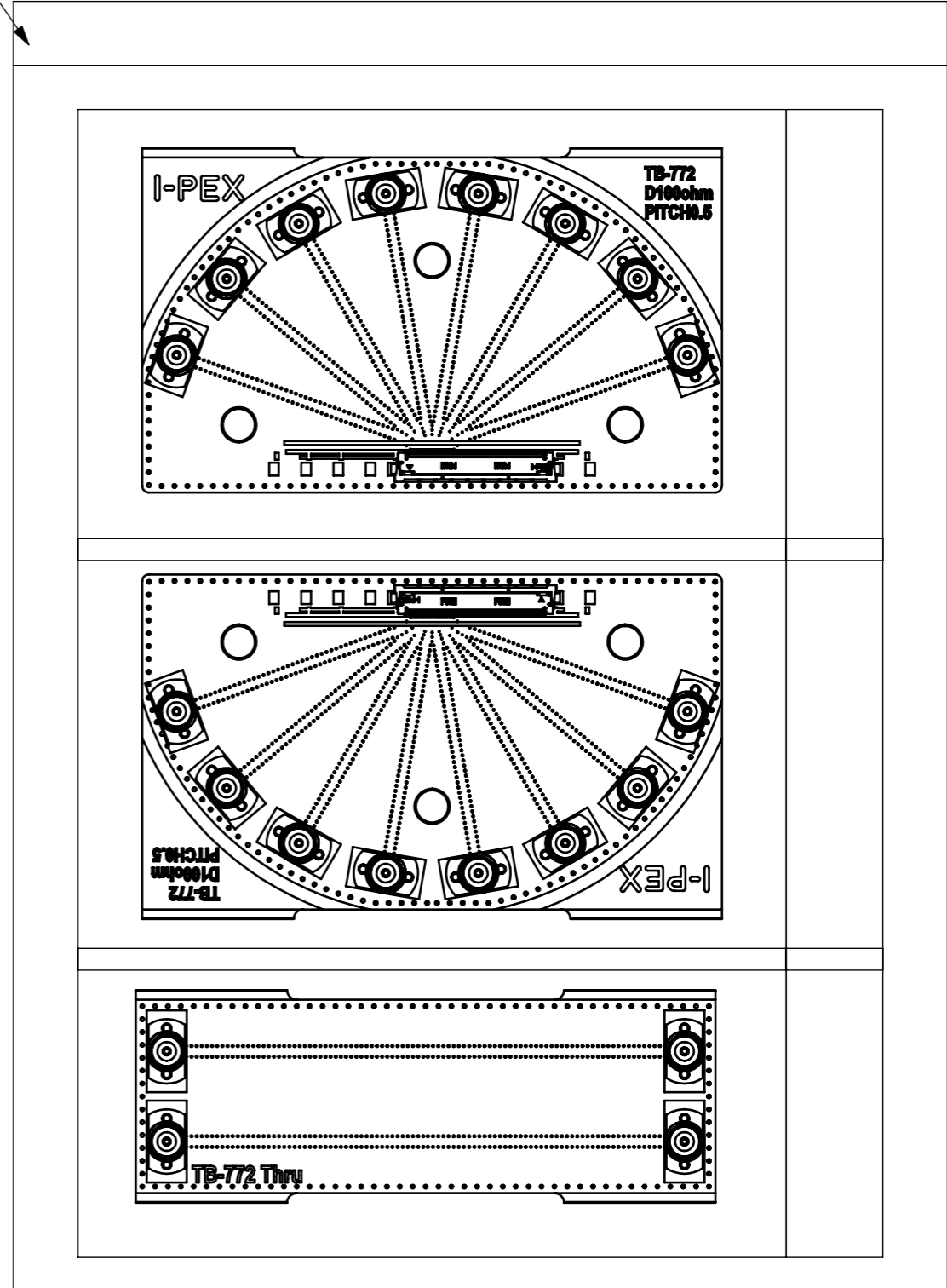
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REUSABLE PLASTIC BAG



<SMALL BAG>  
REUSABLE PLASTIC BAG



<SMALL BAG>  
REUSABLE PLASTIC BAG



ANGLE	±2°	6 OVER 30 MAX.	±0.3	PROJECTION ⊕	SERIES No. -	CUSTOMER COPY		
	6 MAX.	±0.2	30 OVER 120 MAX.					±0.5
GENERAL TOLERANCE.				TITLE		SCALE	<b>I-PEX</b>	
DWG.	DATE			EVAFLEX® 5-VS SI TEST BOARD ASSEMBLY		1:1		
CHK.				DWG. No. 91570		UNIT mm		
APP.						SIZE A3	SHEET 4/4	REV. 1

**PACKING STYLE**